

Figure 3 latch content optimized cell content with latch specific defect

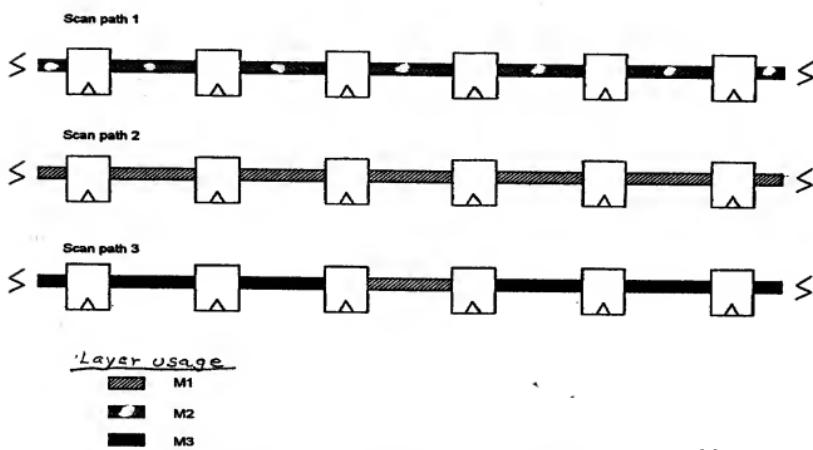


Figure 4: scan paths using restricted wiring layers

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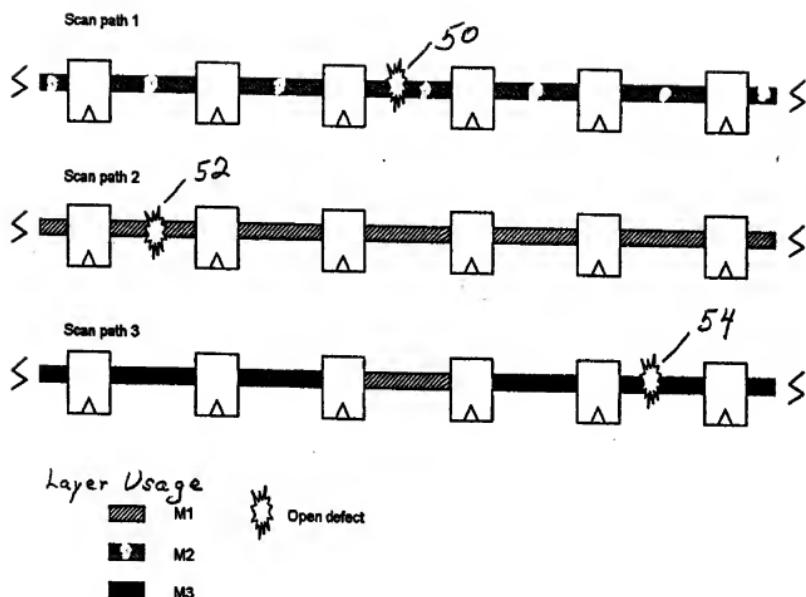


Figure 5: layer optimized cell content with layer specific defects

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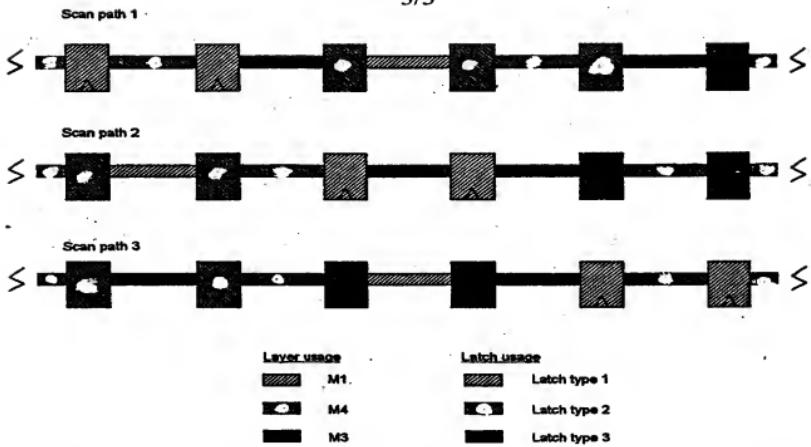


Figure 1: current scan path cell an layer usage

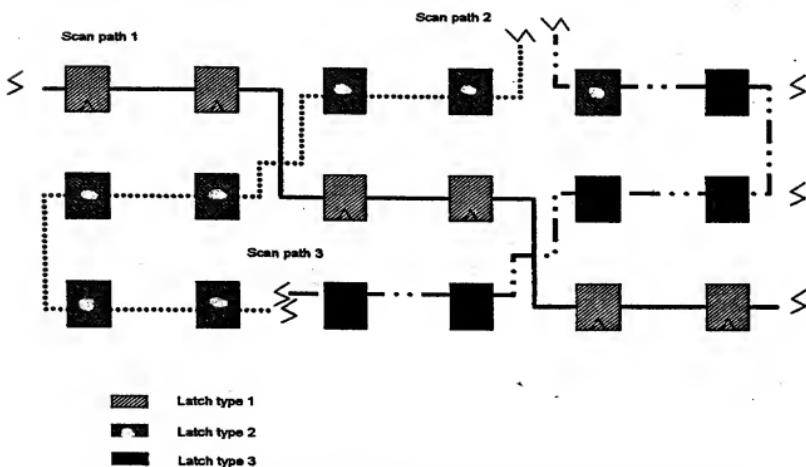


Figure 2 scan path chaining which groups latches of similar types